

Notice of References Cited	Application/Control No. 10/010,239	Applicant(s)/Patent Under Reexamination BLATT ET AL.	
	Examiner A. M. Thompson	Art Unit 2825	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,625,803	04-1997	McNelly et al.	703/14
	B	US-5,692,160	11-1997	Sarin	703/23
	C	US-5,838,947	11-1998	Sarin	703/14
	D	US-			
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	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Katkoori et al., Simulation based Architectural Power Estimation for PLA-based Controllers, Internation Symposium on Low Power Electronics and Design, pages 121-124, August 1996.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.